

Master Blaster AMD SVI3.0 GPU

- Digital Power Management Bus Blaster
- SVI3/ SVID/ AVS/ PMBus Compliant (requires specific plugin)
- FW upgradable through remote server
- Works with ProGrAnalog LoadSlammer standalone GUI
- Works with ProGrAnalog Adj GUI
- Full read/ write register support.
- Supports Telemetry
- API Support

Kit Contents:



Part Number	Part Description	Quantity
MB-0001	Master Blaster AMD SVI3.0 GPU - Device Only	Qty 1
USB-C Cable 2m	USB 2.0 Type A to USB-C Cable, 2m	Qty 1
540-00012	SVI3 Rectangular Cable Assemblies 0.80 mm Double Row Tiger Eye Discrete Wire Teflon Cable Assembly, Socket	Qty 1

Mechanical Specifications:



(Dimensions: 90 x 45 x 12 mm)



Electrical Specifications:

- USB 3.0 and USB C compliant
- Voltage: 5V
- Current: 1A
- No external power supply required.

Additional:

Weight	28g
ROHS Compliant	Yes
Shipping Info	HTS-9030.20.1000
Shipping Info	ECCN-3B992
Country of Origin	United States

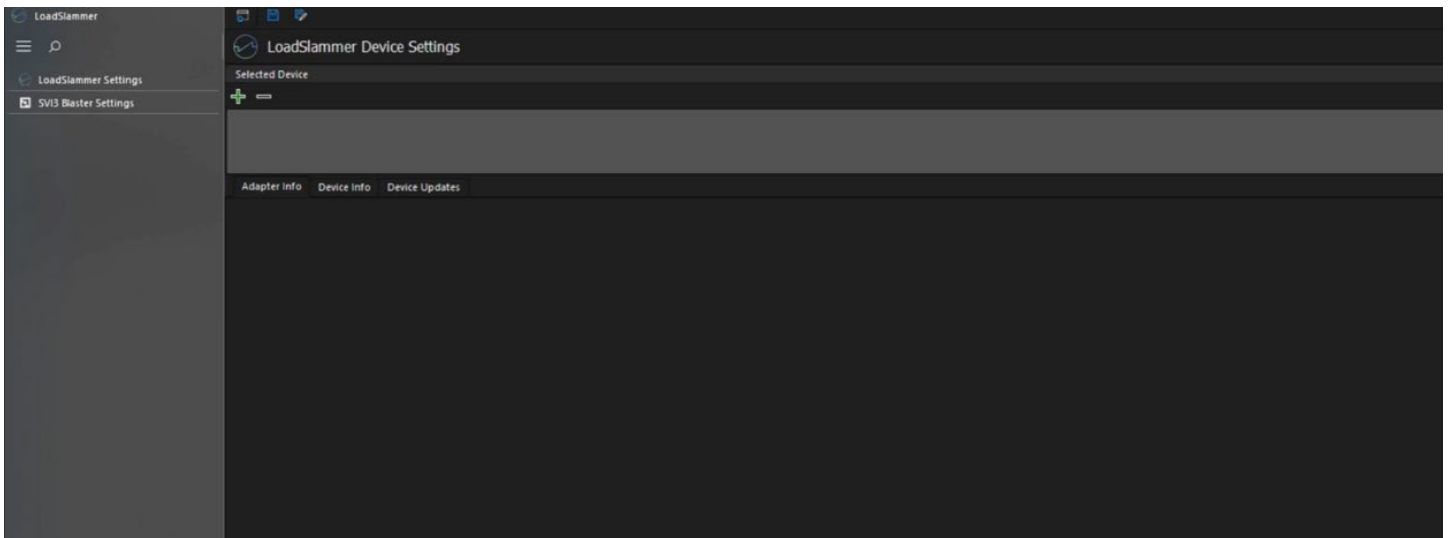


Power-On Current for Zynq-7000 Devices

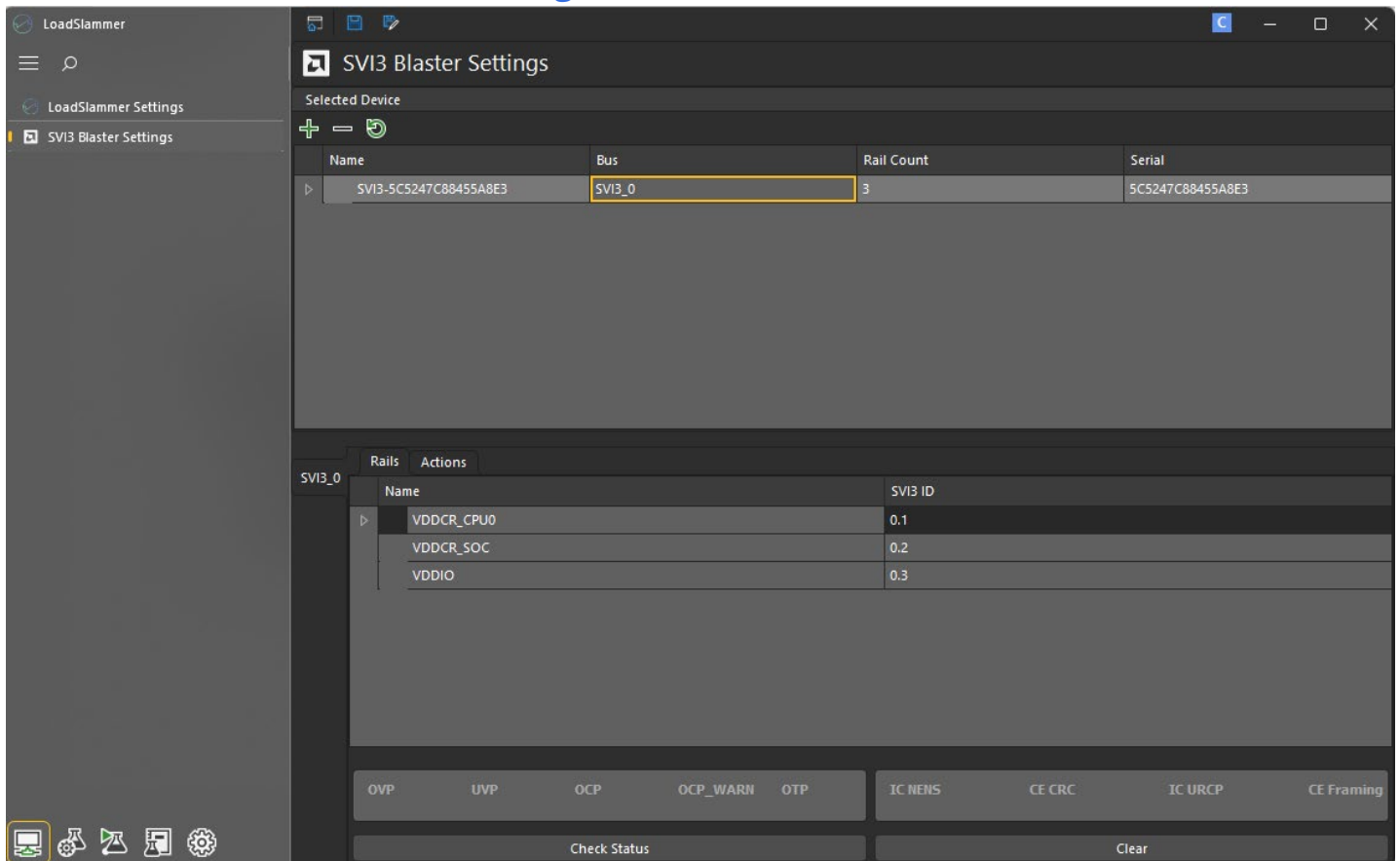
Device	I _{CCPINTMIN}	I _{CCPAUXMIN}	I _{CCDDRMIN}	I _{CCINTMIN}	I _{CCAUXMIN}	I _{CCOMIN}	I _{CCAUX_IOMIN}	I _{CCBRAMMIN}	Units
XC7Z030	I _{CCPINTQ} + 70 mA	I _{CCPAUXQ} + 40 mA	I _{CCDDRQ} + 130 mA per bank	I _{CCINTQ} + 900 mA	I _{CCAUXQ} + 60 mA	I _{CCOQ} + 90 mA per bank	I _{CCOAUXXIOQ} + 40 mA per bank	I _{CCBRAMQ} + 90 mA	mA
XC7Z035	I _{CCPINTQ} + 70 mA	I _{CCPAUXQ} + 40 mA	I _{CCDDRQ} + 130 mA per bank	I _{CCINTQ} + 1400 mA	I _{CCAUXQ} + 60 mA	I _{CCOQ} + 90 mA per bank	I _{CCOAUXXIOQ} + 40 mA per bank	I _{CCBRAMQ} + 90 mA	mA
XC7Z045	I _{CCPINTQ} + 70 mA	I _{CCPAUXQ} + 40 mA	I _{CCDDRQ} + 130 mA per bank	I _{CCINTQ} + 1400 mA	I _{CCAUXQ} + 60 mA	I _{CCOQ} + 90 mA per bank	I _{CCOAUXXIOQ} + 40 mA per bank	I _{CCBRAMQ} + 90 mA	mA
XC7Z100	I _{CCPINTQ} + 70 mA	I _{CCPAUXQ} + 40 mA	I _{CCDDRQ} + 130 mA per bank	I _{CCINTQ} + 2200 mA	I _{CCAUXQ} + 60 mA	I _{CCOQ} + 90 mA per bank	I _{CCOAUXXIOQ} + 40 mA per bank	I _{CCBRAMQ} + 90 mA	mA
XA7Z030	I _{CCPINTQ} + 70 mA	I _{CCPAUXQ} + 40 mA	I _{CCDDRQ} + 130 mA per bank	I _{CCINTQ} + 900 mA	I _{CCAUXQ} + 60 mA	I _{CCOQ} + 90 mA per bank	I _{CCOAUXXIOQ} + 40 mA per bank	I _{CCBRAMQ} + 90 mA	mA
XQ7Z030	I _{CCPINTQ} + 70 mA	I _{CCPAUXQ} + 40 mA	I _{CCDDRQ} + 130 mA per bank	I _{CCINTQ} + 900 mA	I _{CCAUXQ} + 60 mA	I _{CCOQ} + 90 mA per bank	I _{CCOAUXXIOQ} + 40 mA per bank	I _{CCBRAMQ} + 90 mA	mA
XQ7Z045	I _{CCPINTQ} + 70 mA	I _{CCPAUXQ} + 40 mA	I _{CCDDRQ} + 130 mA per bank	I _{CCINTQ} + 1400 mA	I _{CCAUXQ} + 60 mA	I _{CCOQ} + 90 mA per bank	I _{CCOAUXXIOQ} + 40 mA per bank	I _{CCBRAMQ} + 90 mA	mA
XQ7Z100	I _{CCPINTQ} + 70 mA	I _{CCPAUXQ} + 40 mA	I _{CCDDRQ} + 130 mA per bank	I _{CCINTQ} + 2200 mA	I _{CCAUXQ} + 60 mA	I _{CCOQ} + 90 mA per bank	I _{CCOAUXXIOQ} + 40 mA per bank	I _{CCBRAMQ} + 90 mA	mA



Opening GUI



GUI with SVI3 Blaster settings



GUI with telemetry settings

LoadSlammer

Platform Overview

Pass/Fail Settings

Telemetry

Static Load

Dynamic Load

Telemetry Configuration

Rail	Clock Speed	Poll Rate	Telemetry
☒ VDDCR_CPU0	20 MHz	10 kHz	OutputCurrent, OutputVoltage, Temp1
☐ VDDCR_CPU1	20 MHz	10 kHz	OutputCurrent, OutputVoltage, Temp1
☐ VDDCR_SOC	20 MHz	10 kHz	OutputCurrent, OutputVoltage, Temp1
☐ VDDIO	20 MHz	10 kHz	OutputCurrent, OutputVoltage, Temp1
☐ VDD_11_S3	20 MHz	10 kHz	OutputCurrent, OutputVoltage, Temp1

